

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Hideaki WATANABE, et al.

Serial No. 09/973,767

Group Art Unit: 2816

Confirmation No. 7559

Filed: October 11, 2001

Examiner: K.B. Wells



For: DIFFERENTIAL SIGNAL OUTPUT APPARATUS, SEMICONDUCTOR INTEGRATED CIRCUIT APPARATUS HAVING THE DIFFERENTIAL SIGNAL OUTPUT APPARATUS, DIFFERENTIAL SIGNAL TRANSMISSION SYSTEM, SIGNAL DETECTION APPARATUS, SIGNAL DETECTION METHOD, SIGNAL TRANSMISSION SYSTEM AND COMPUTER-READABLE PROGRAM

AMENDMENT

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

This is in response to the Office Action mailed August 22, 2003, and having a period for response set to expire on November 22, 2003.

The following amendments and remarks are respectfully submitted. Reconsideration of the claims is respectfully requested.

RECEIVED
NOV 28 2003
TECHNOLOGY CENTER 2800